### 5A SURFACE MOUNT LOW VF SCHOTTKY BRIDGE RECTIFIER

#### **FEATURES:**

- Reverse Voltage 60 V
- Forward Current 5.0 A
- · Low power loss, high efficiency
- · High Surge Current Capability
- For use in low voltage ,high frequency inverters, free wheeling, and polarity protection applications
- Designed for Surface Mount Application

#### **MECHANICAL DATA**

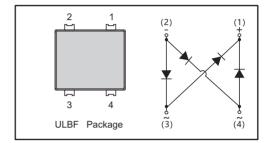
· Case: ULBF

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.461g / 0.0163oz

#### **PINNING**

PIN	DESCRIPTION	
1	Output Anode (+)	
2	Output Cathode (-)	
3	Input Pin(~)	
4	Input Pin(~)	



#### Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	ULBFL56SM	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	60	V
Maximum RMS voltage	$V_{RMS}$	42	٧
Maximum DC Blocking Voltage	V <sub>DC</sub>	60	V
Maximum Average Forward Rectified Current @ Fig.1	I <sub>F(AV)</sub>	5.0	А
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	100	Α
Peak Forward Surge Current 1.0 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	200	Α
I²t Rating for fusing (3ms≤t≤8.3ms)	l²t	41.5	A <sup>2</sup> S
Maximum Forward Voltage at 3 A at 5 A	V <sub>F</sub>	0.45(TYP) 0.59	V
Maximum DC Reverse Current T <sub>a</sub> = 25 °C at Rated DC Blocking Voltage T <sub>a</sub> =100 °C	I <sub>R</sub>	0.3 50	mA
Typical Junction Capacitance (1)	C <sub>j</sub>	645	pF
Typical Thermal Resistance (2)	$R_{ heta JA} \ R_{ heta JC} \ R_{ heta JL}$	32 7 12	°C/W
Operating and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150	°C

<sup>(1)</sup> Measured at 1 MHz and applied reverse voltage of 4 V D.C

<sup>(2)</sup> Mounted on glass epoxy PC board with 4×1.5"×1.5" (3.81×3.81 cm) copper pad.

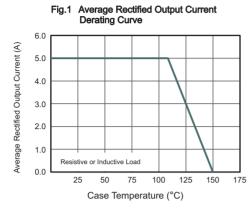




Fig.2 Typical Reverse Characteristics

10<sup>4</sup>

10<sup>3</sup>

95

10<sup>2</sup>

97

10<sup>2</sup>

97

10<sup>2</sup>

97

10<sup>2</sup>

97

10<sup>2</sup>

97

10<sup>3</sup>

97

10<sup>3</sup>

97

10<sup>3</sup>

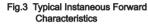
98

10<sup>3</sup>

90

10<sup>3</sup>

1



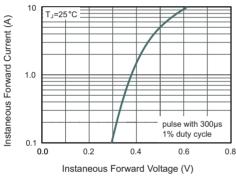


Fig.4 Typical Junction Capacitance

Percent of Rated Peak Reverse Voltage (%)

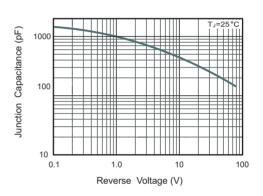
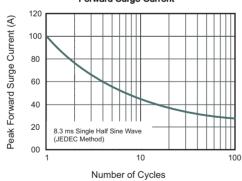


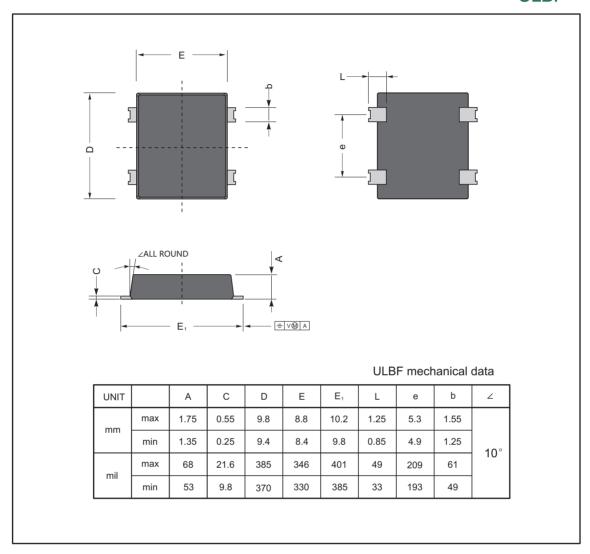
Fig.5 Maximum Non-Repetitive Peak Forward Surge Current



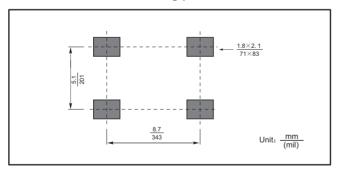
## PACKAGE OUTLINE

## Plastic surface mounted package; 4 leads

# **ULBF**



## The recommended mounting pad size



### Marking

Type number	Marking code
ULBFL56SM	L56S

## Important Notice and Disclaimer

Jingdao Microelectronics reserves the right to make changes to this document and its products and specifications at any time without notice.

Customers should obtain and confirm the latest product information and specifications before final design, purchase or use.

Jingdao Microelectronics makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, not does Jingdao Microelectronics assume any liability for application assistance or customer product design.

Jingdao Microelectronics does not warrant or accept any liability with products which are purchased or used for any unintended or unauthorized application.

No license is granted by implication or otherwise under any intellectual property rights of Jingdao Microelectronics.

Jingdao Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of Jingdao Microelectronics.